GREEGOO **KS100N12… SERIES**

BI-DIRECTIONAL THYRISTOR

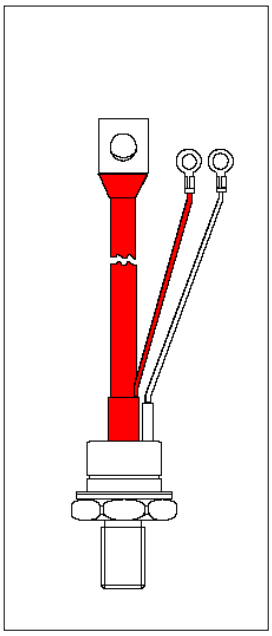
100A

Features

- Hermetic ceramic -metal seal
- high dv/dt
- tested according to IEC standards
- High surge capability
- Compression Bonded Encapsulation for heavy duty operations such as severe thermal cycling

Typical Applications

- DC motor controls
- Controlled DC power supplies
- AC controllers



Major Ratings and Characteristics

Parameters		KS100N12	Units
$I_{T(AV)}$		100	A
	@ T_C	90	°C
$I_{T(RMS)}$		155	A
I_{TSM}	@ 50Hz	2.7	A
	@ 60Hz	2.83	A
I^2t	@ 50Hz	36.4	KA^2s
	@ 60Hz	33.2	KA^2s
V_{DRM} /V_{RRM}		400 to 1600	V
Tq	typical	300	μs
T_J	range	- 40 to 125	°C

ELECTRICAL SPECIFICATIONS

Voltage Ratings

Type number	Voltage Code	V_{RRM} /V_{DRM}, maximum repetitive peak reverse voltage V	V_{RSM}, maximum non-repetitive peak rev. voltage V	I_{RRM} /I_{DRM} max. @ T_J = T_J max. mA
KS100N	02	200	300	20
	06	600	700	
	10	1000	1100	
	12	1200	1300	
	15	1500	1600	

On-state Conduction

Parameter		KS100N	Units	Conditions		
$I_{T(AV)}$	Maximum average on-state current	100	A	180° conduction, half sine wave		
	@ Case temperature	90	°C			
$I_{(RMS)}$	Maximum RMS on-state current	175	A	180° conduction, half sine wave @ T_C = 80°C		
I_{TSM},	Maximum peak, one-cycle non-repetitive surge current	2700	A	t = 10ms	No voltage reapplied	Sinusoidal half wave, Initial T = T max.
		2830		t = 8.3ms		
		2270		t = 10ms	100% V_{RRM} reapplied	
		2380		t = 8.3ms		
I^2t	Maximum I^2t for fusing	36.4	KA^2s	t = 10ms	No voltage reapplied	
		33.2		t = 8.3ms		
		25.8		t = 10ms	100% V_{RRM} reapplied	
		23.5		t = 8.3ms		
$I^2\sqrt{t}$	Maximum $I^2\sqrt{t}$ for fusing	364	$KA^2\sqrt{s}$	t = 0.1 to 10ms, no voltage reapplied		
V_{TM}	Maximum on-state or forward	1.52	V	pk = 600A, T_J = 25°C, t p = 10ms sine pulse		
I_H	Maximum holding current	600	mA	T_J = 25°C, anode supply 12V resistive load		
I_L	Typical latching current	1000				

Switching

	Parameter	KS100N	Units	Conditions
di/dt	ax. non-repetitive rate of rise of turned-on current	500	A/μs	Gate drive 20V, 20Ω, $t_r \leq$ 1μs T_J = T_J max, anode voltage ≤ 80% V_{DRM}
td	ical delay time	2.0	μs	Gate current 1A, di_g/dt = 1A/μs V_d = 0.67% V_{DRM}, T_J = 25°C
Tq	pical turn-off time	100	μs	I_{TM} = 300A, T_J = T_J max, di/dt = 20A/μs, V_R = 50V dv/dt = 20V/μs, Gate 0V 100Ω, t_p = 500μs

Blocking

	Parameter	KS100N	Units	Conditions
dv/dt	Maximum critical rate of rise of off-state voltage	500	V/μs	T_J = T_J max linear to 80% rated V_{DRM}
I_{DRM} I_{RRM}	Max. peak reverse and off-state leakage current	20	mA	T_J = T_J max, rated V_{DRM}/V_{RRM} applied

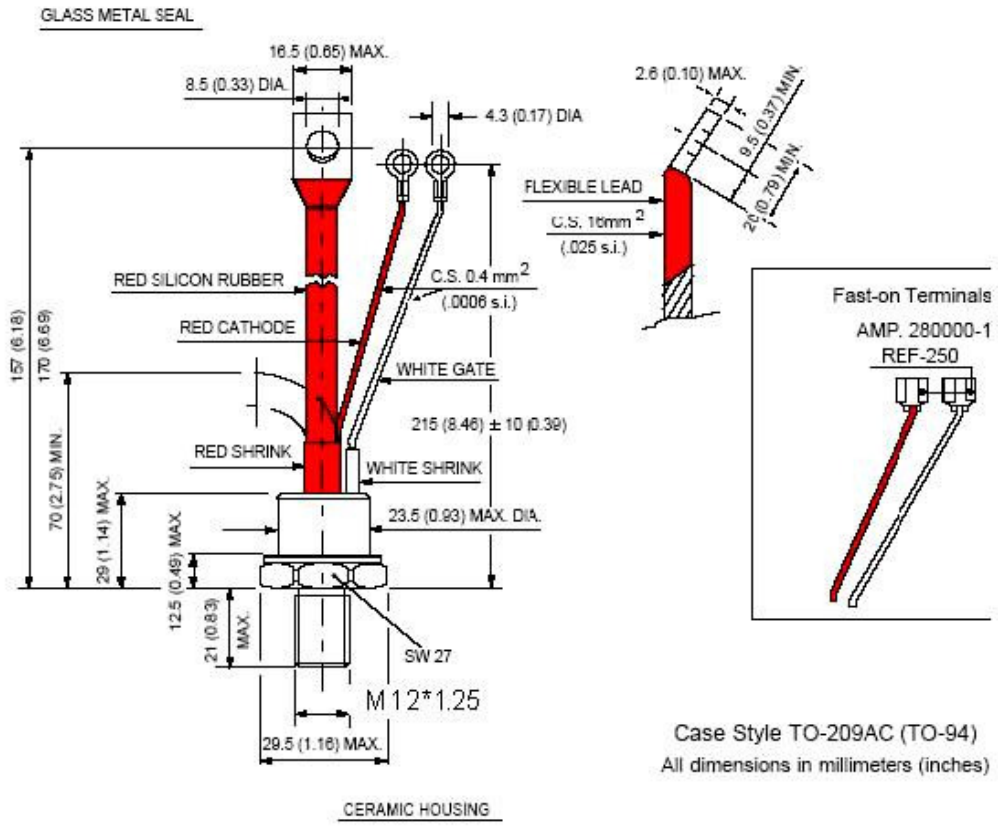
Triggering

	Parameter	KS100N		Units	Conditions	
P_{GM}	Maximum peak gate power	5		W	T_J = T_J max, $t_p \leq 5ms$	
$P_{G(AV)}$	Maximum average gate power	1.0			T_J = T_J max, f = 50Hz, d% = 50	
I_{GM}	Max. peak positive gate current	2.0		A	T_J = T_J max, $t_p \leq 5ms$	
$+V_{GM}$	Maximum peak positive gate voltage	20		V	T_J = T_J max, $t_p \leq 5ms$	
$-V_{GM}$	Maximum peak negative gate voltage	5.0				
I_{GT}	DC gate current required to trigger	TYP.	MAX.	mA		
		180	-		T_J = - 40°C	Max. required gate trigger/ current/ voltage are the lowest value which will trigger all units 12V anode-to-cathode applied
		90	150		T_J = 25°C	
		40	-		T_J = 125°C	
V_{GT}	DC gate voltage required to trigger	2.9	-	V	T_J = - 40°C	
		1.8	30		T_J = 25°C	
		1.2	-		T_J = 125°C	
I_{GD}	DC gate current not to trigger	8		mA	T_J = T_J max	Max. gate current/ voltage not to trigger is the max. value which will not trigger any unit with rated V anode-to-cathode applied
V_{GD}	DC gate voltage not to trigger	0.25		V		

Thermal and Mechanical Specification

	Parameter	KS100N	Units	Conditions
T_J	Max. operating temperature range	-40 to 125	°C	
T_{stg}	Max. storage temperature range	-40 to 150		
R_{thJC}	Max. thermal resistance, junction to case	0.195	K/W	DC operation
R_{thCS}	Max. thermal resistance, case to heatsink	0.08		Mounting surface, smooth, flat and greased
T	Mounting torque, ± 10%	15.5(137)	Nm	Non lubricated threads
		14(120)	(lbf-in)	Lubricated threads
wt	Approximate weight	118	g	

Outline Table



Case Style TO-209AC (TO-94)

All dimensions in millimeters (inches)

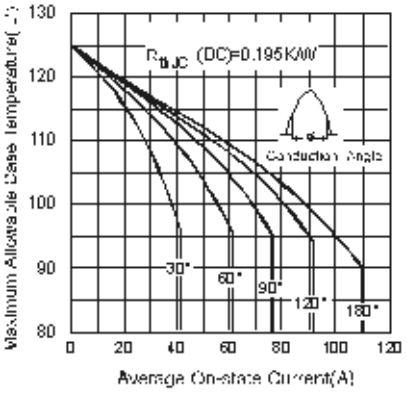


Fig.1-Current Rating Characteristics

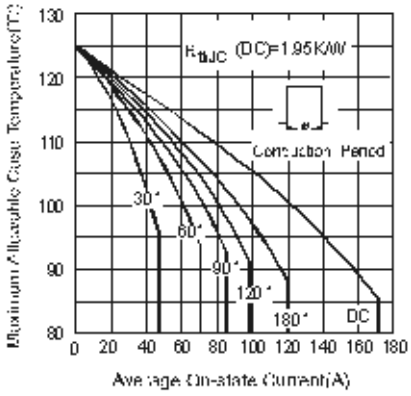


Fig.2-Current Rating Characteristics

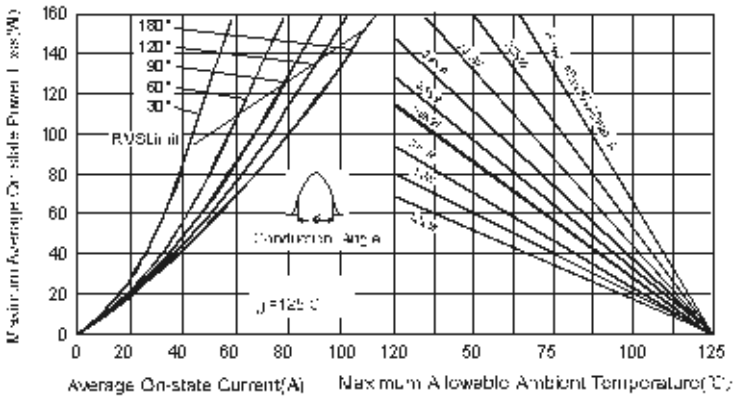


Fig.3-On-state Power Loss Characteristics

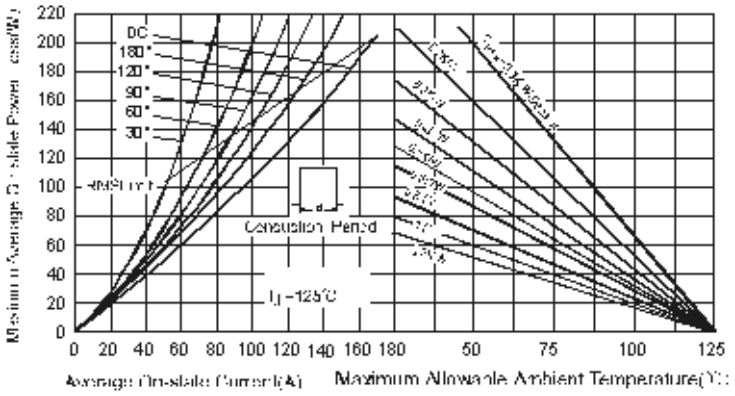


Fig.4-On-state Power Loss Characteristics

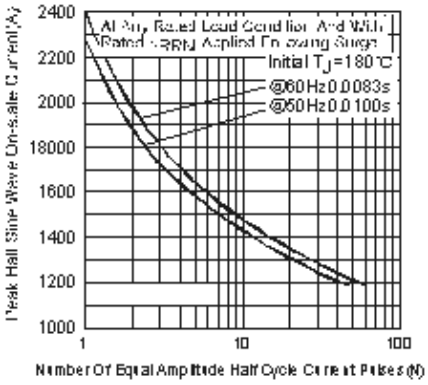


Fig.5-Maximum Non-Repetitive Surge Current

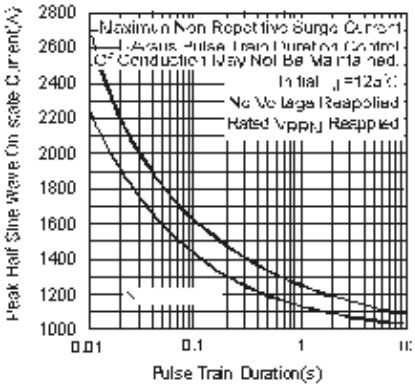


Fig.6-Maximum Non-Repetitive Surge Current

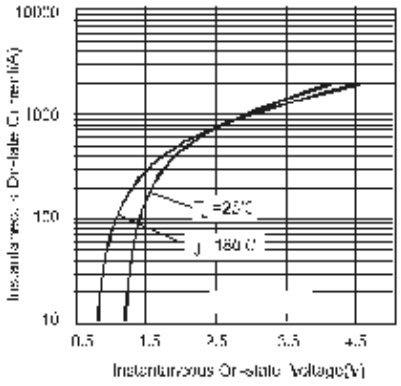


Fig.7-On-state Voltage Drop Characteristics

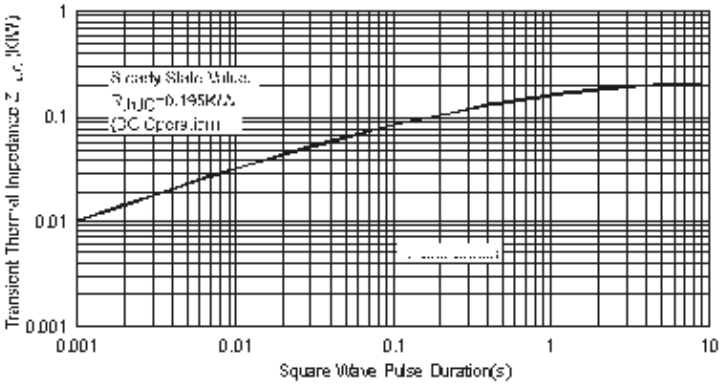


Fig.8-Thermal Impedance Z_{thJC} Characteristics